

Let There Be Topology-Awareness in Kube-Scheduler!!

Swati Sehgal

Senior Software Engineer

KNI Telco-5G Compute team

Email: swsehgal@redhat.com

Slack: [swsehgal](#)

Github: [swatisehgal](#)

Today's Agenda

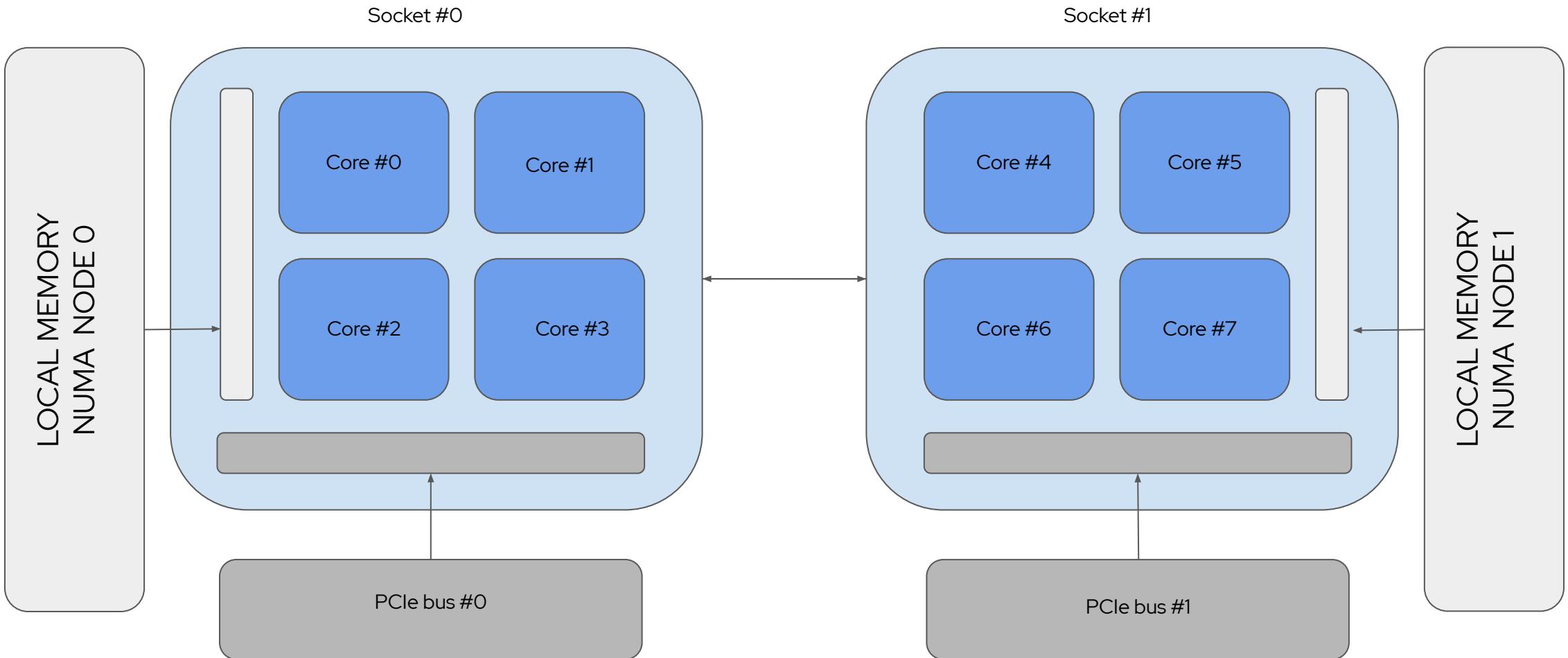
1. Hardware Topology
2. Topology Alignment in Kubernetes
3. Topology-unawareness of Kubernetes Default Scheduler
4. How the Kubernetes Default Scheduler works
5. Topology aware scheduling - proposed Solution
6. Use cases

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Hardware Topology: What is NUMA ?



Why is NUMA Alignment needed?

NUMA alignment of CPUs and devices allows workloads to run in an environment optimized for low-latency.

- ▶ Application in the field of Telco 5G, ML, AI and data analytics require NUMA alignment
- ▶ DPDK based networking applications require resources from the same NUMA node for optimal performance

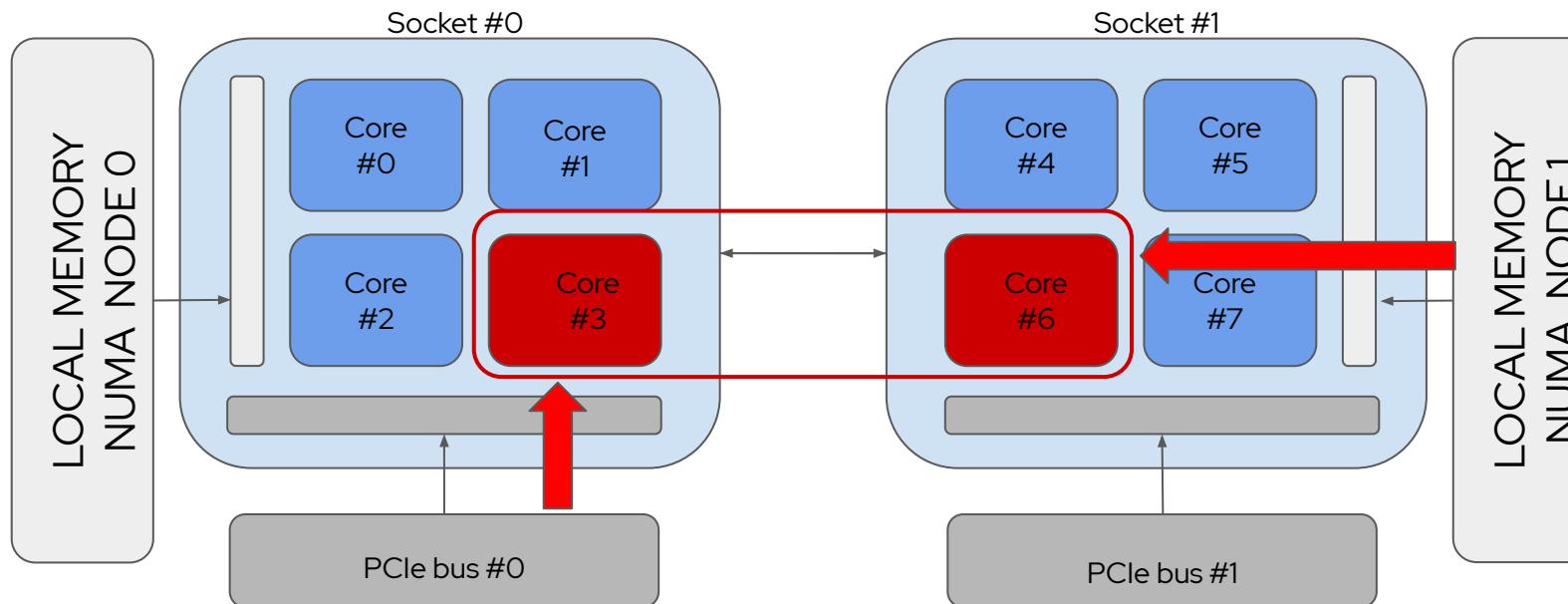
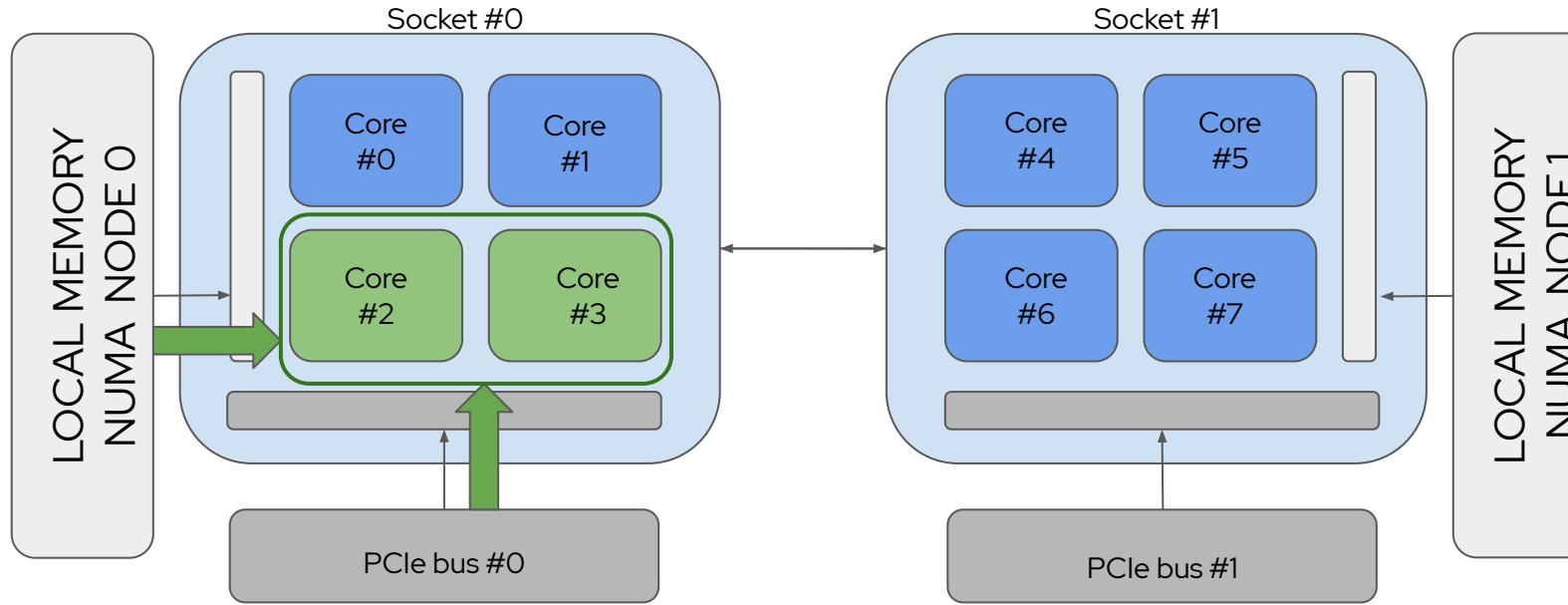
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NUMA Alignment in Kubernetes

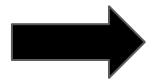
7



```
kind: Pod
metadata:
  name: my-pod
spec:
  containers:
  - name: my-container
    resources:
      requests:
        example.com/deviceA: 1
        memory: "64Mi"
        cpu: 2
      limits:
        example.com/deviceA: 1
        memory: "64Mi"
        cpu: 2
```

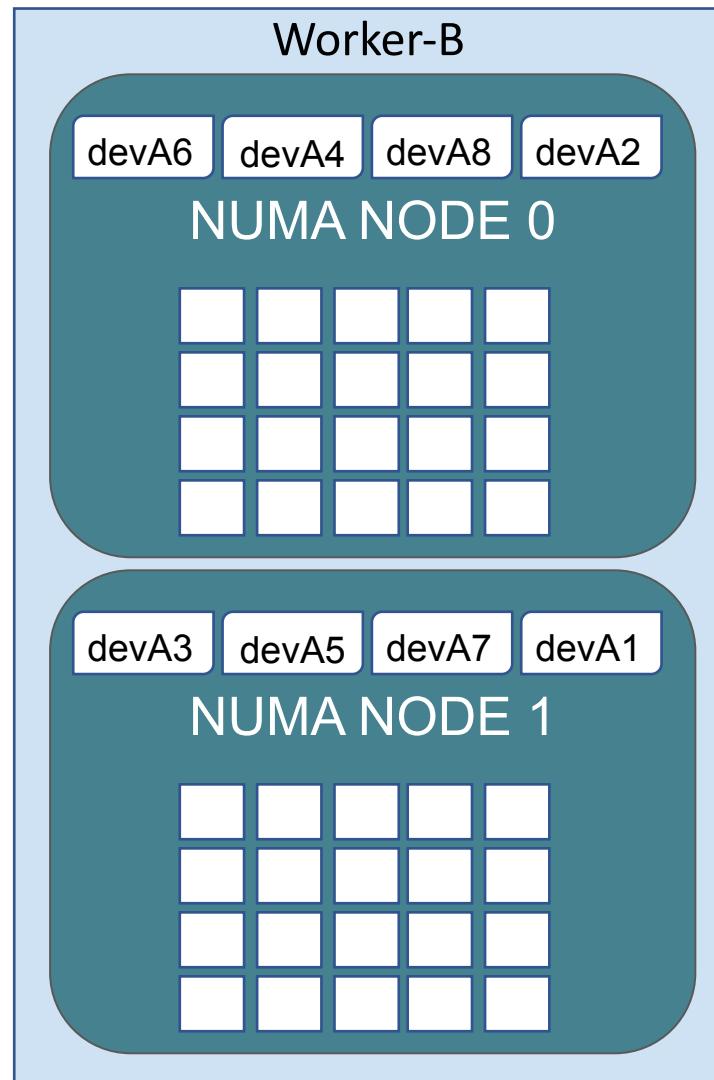
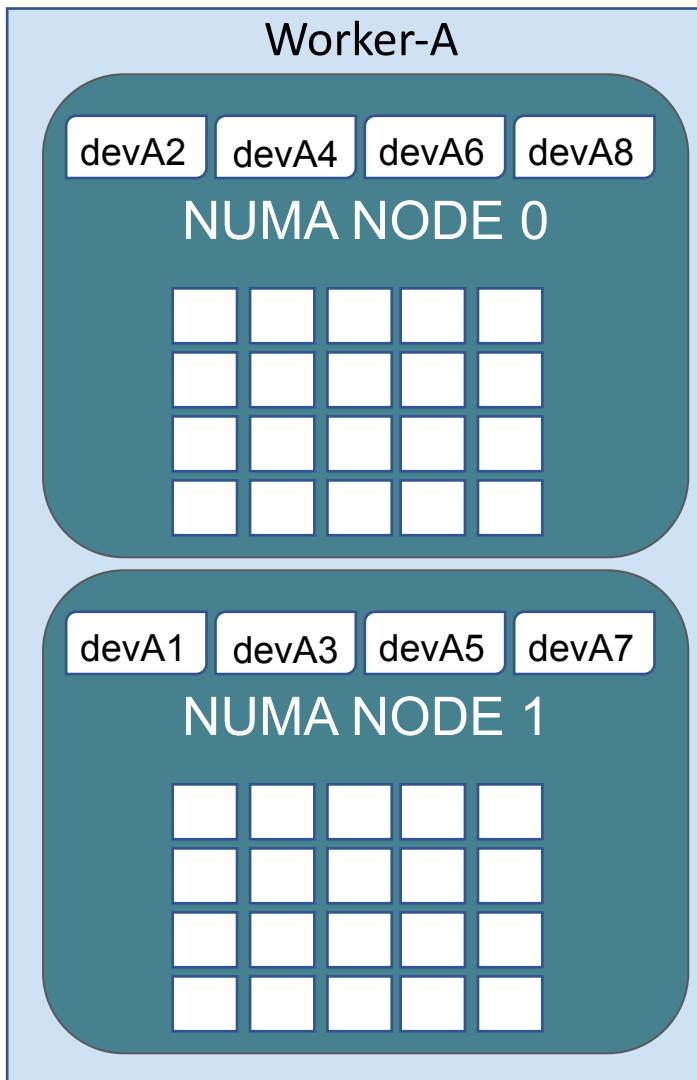


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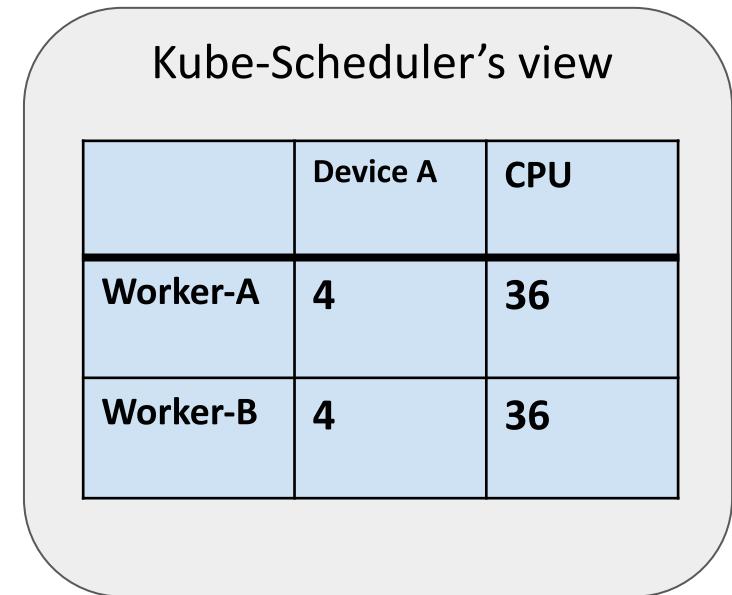
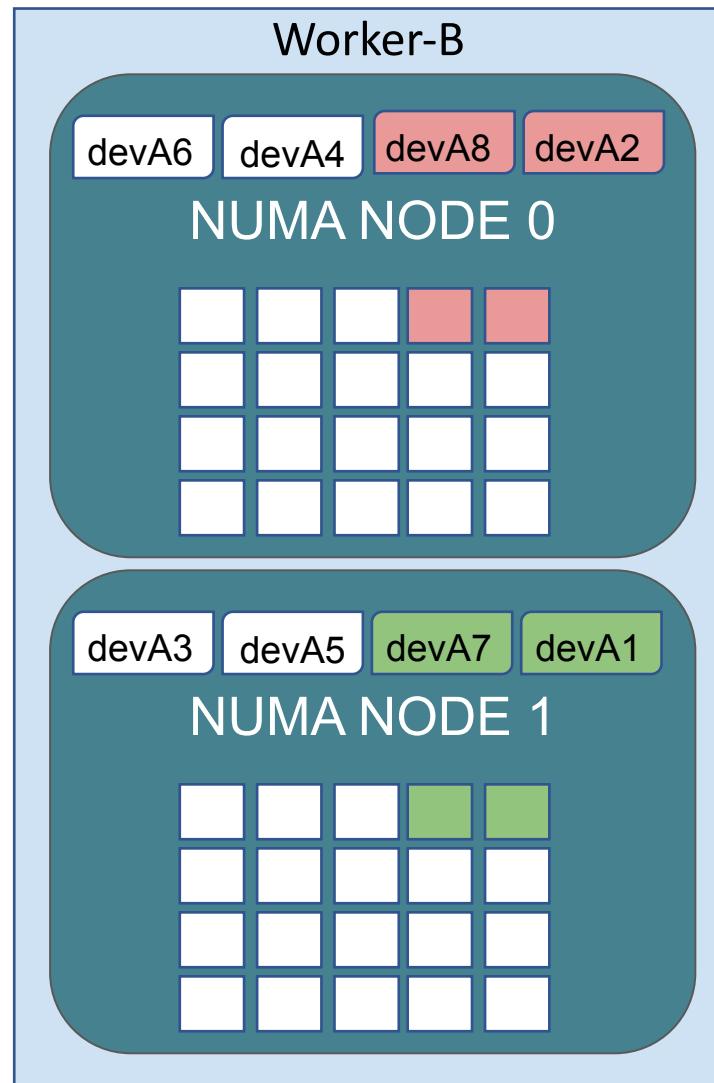
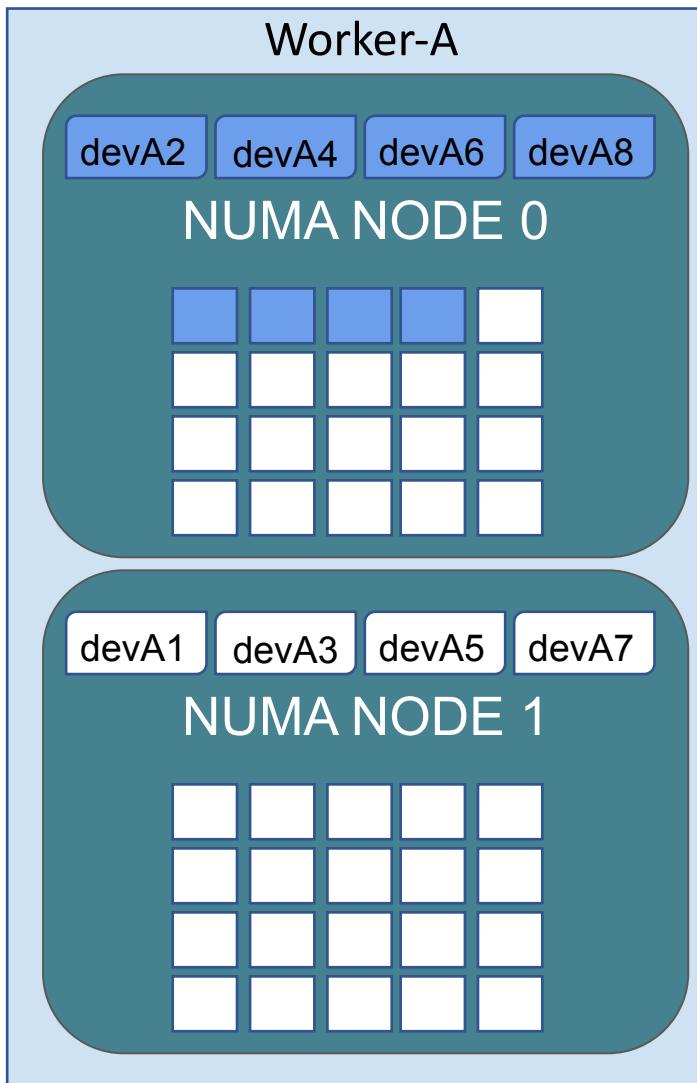
Topology-unawareness of Kubernetes Default Scheduler



Kube-Scheduler's view

	Device A	CPU
Worker-A	8	40
Worker-B	8	40

Topology-unawareness of Kubernetes Default Scheduler

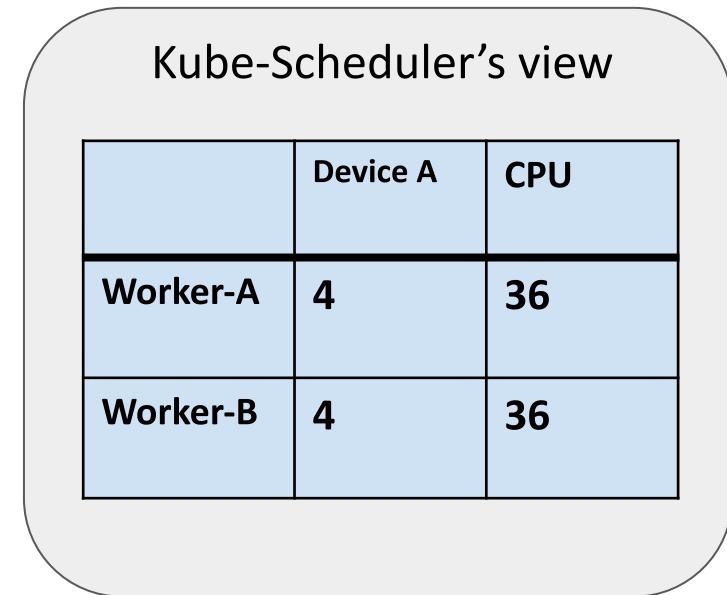
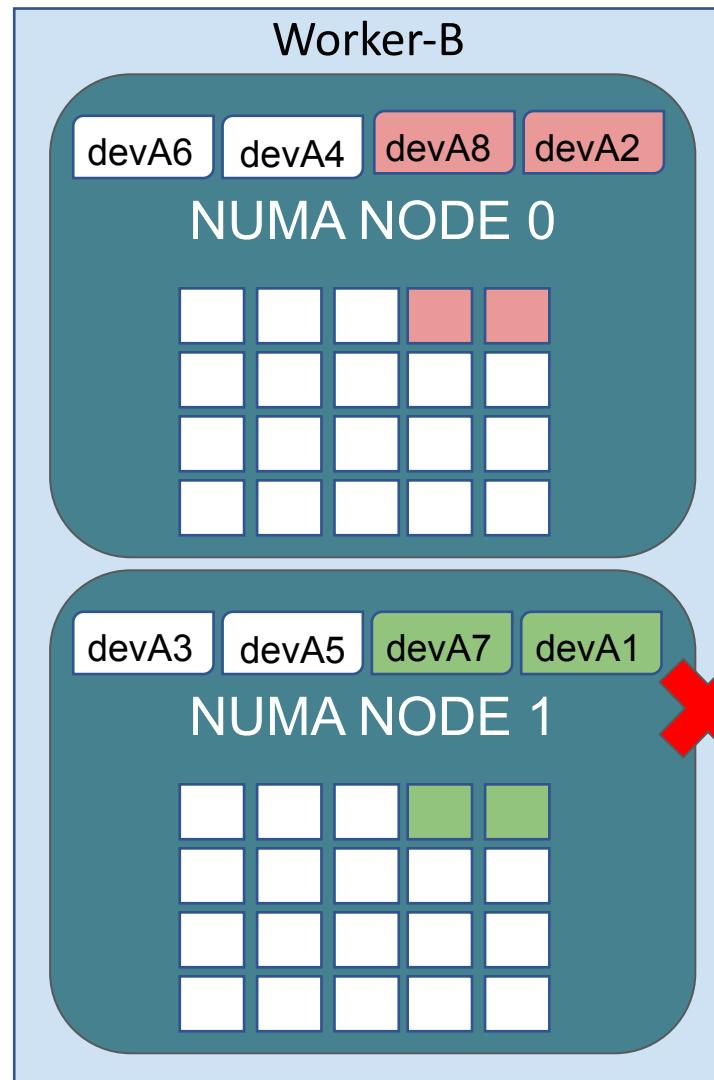
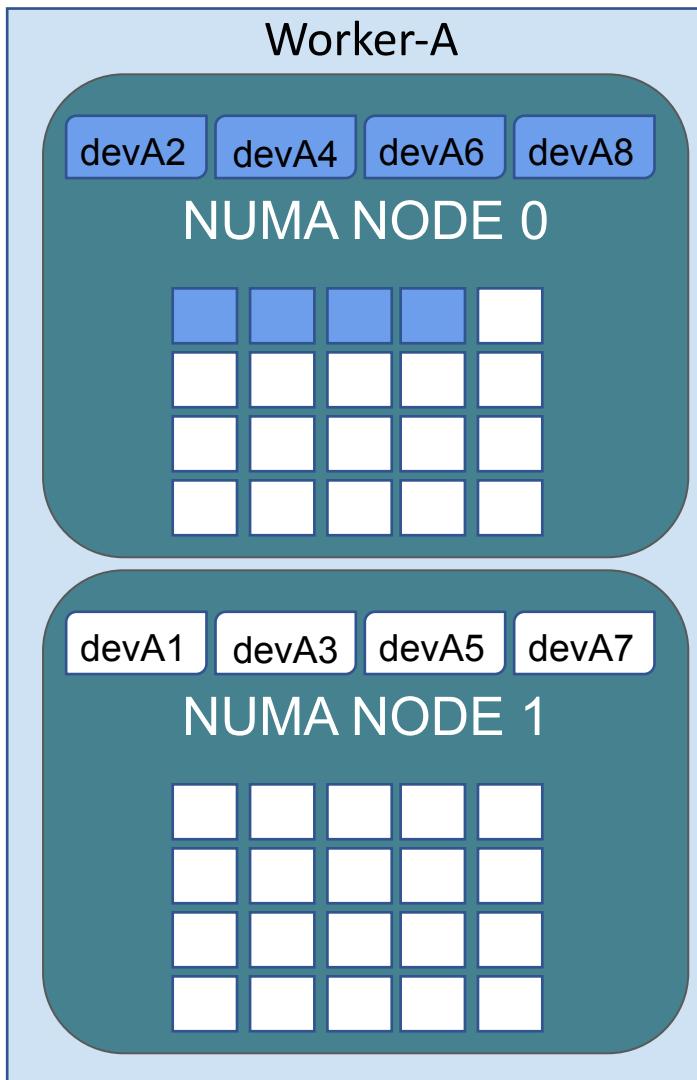


```
resources:  
  requests:  
    deviceA: 2  
    Cpu: 2  
    memory: 200Mi  
limits:  
  deviceA: 2  
  cpu: 2  
  memory: 200Mi
```

```
resources:  
  requests:  
    deviceA: 2  
    cpu: 2  
    memory: 200Mi  
limits:  
  deviceA: 2  
  cpu: 2  
  memory: 200Mi
```

```
resources:  
  requests:  
    deviceA: 5  
    cpu: 4  
    memory: 200Mi  
limits:  
  deviceA: 4  
  cpu: 4  
  memory: 200Mi
```

Topology-unawareness of Kubernetes Default Scheduler



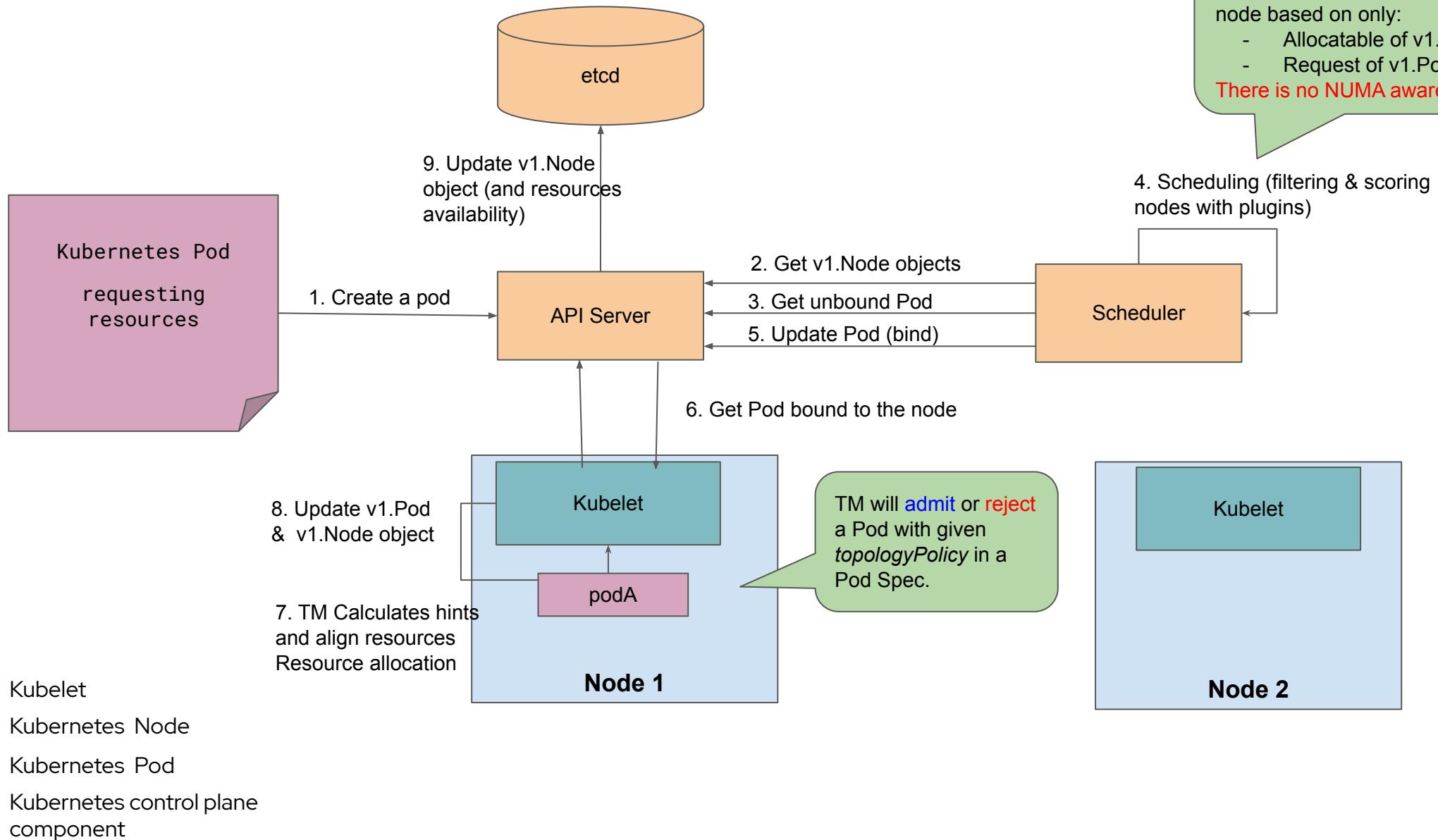
```
kind: Pod
metadata:
  name: pod-with-my-scheduler
spec:
  schedulerName: my-scheduler
  containers:
  - name: pod-with-my-scheduler
    resources:
      requests:
        example.com/deviceA: 4
        cpu: 4
      limits:
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How the Kubernetes Default Scheduler works



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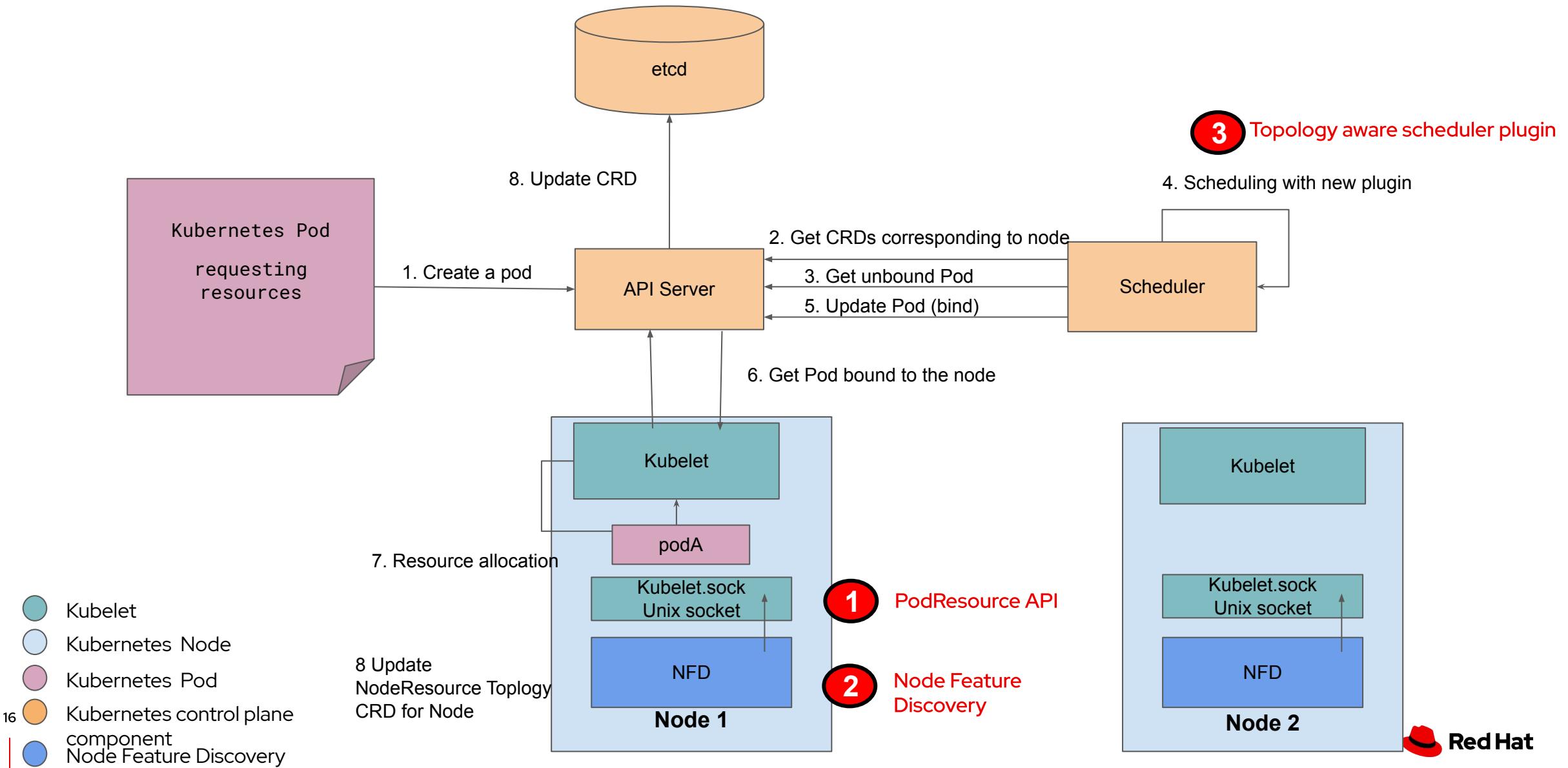


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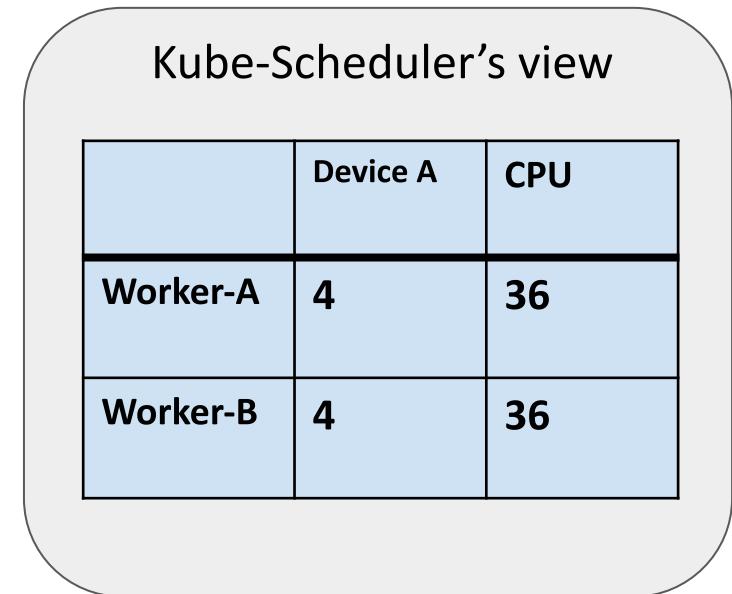
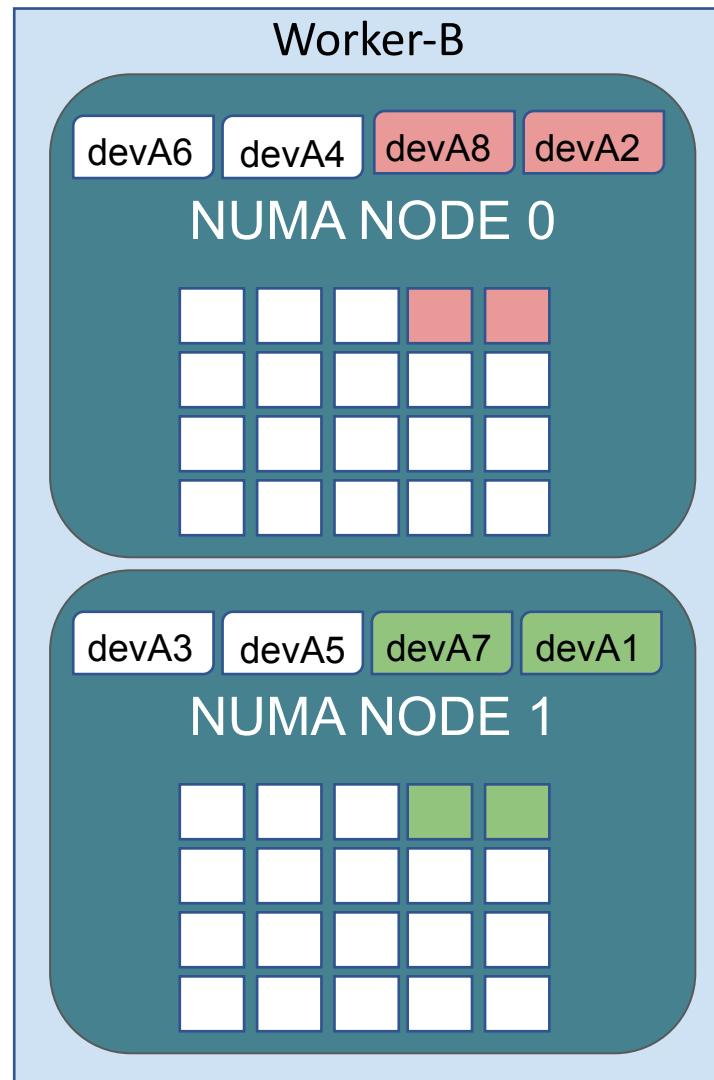
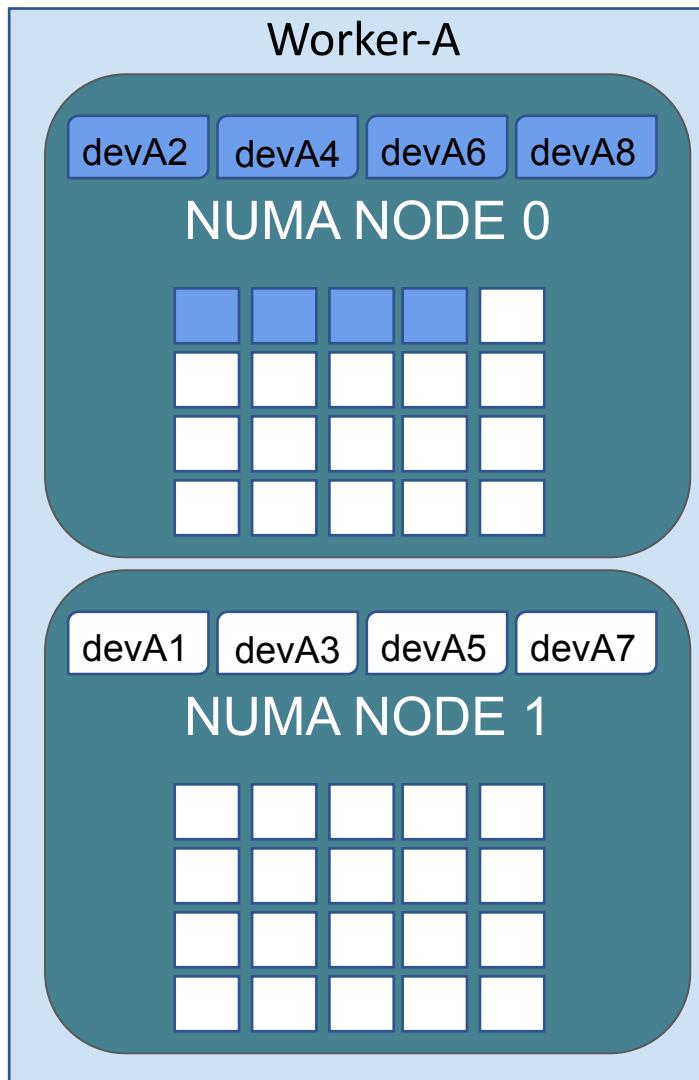
Key components of Topology aware scheduling

1. PodResource API
2. Node Feature Discovery
3. Topology aware scheduler plugin
4. NodeResourceTopology API

End to end Proposed Solution: Topology Aware Scheduling



Topology-unawareness of Kubernetes Default Scheduler

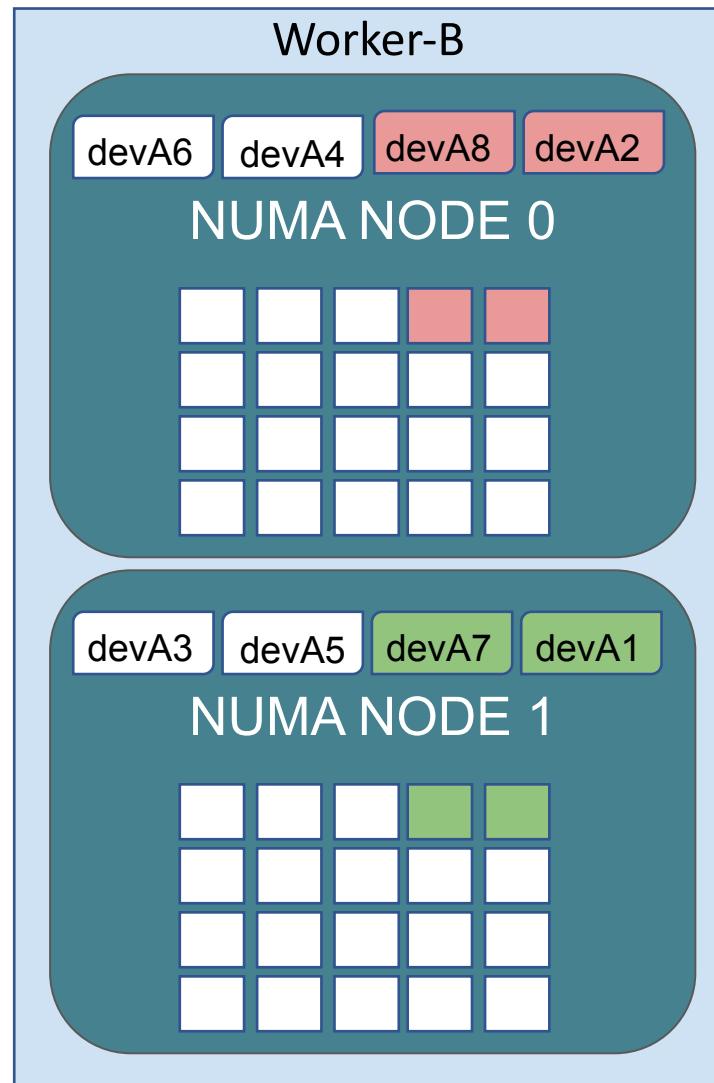
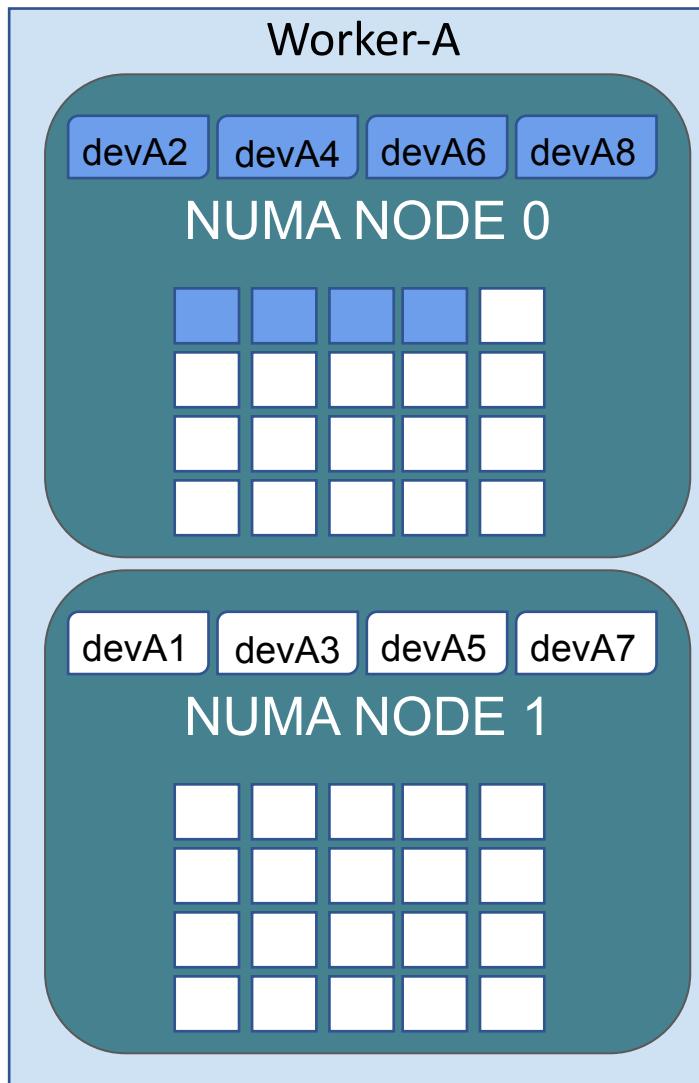


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Topology-aware Scheduling in Kubernetes



Topology-aware Scheduler's view

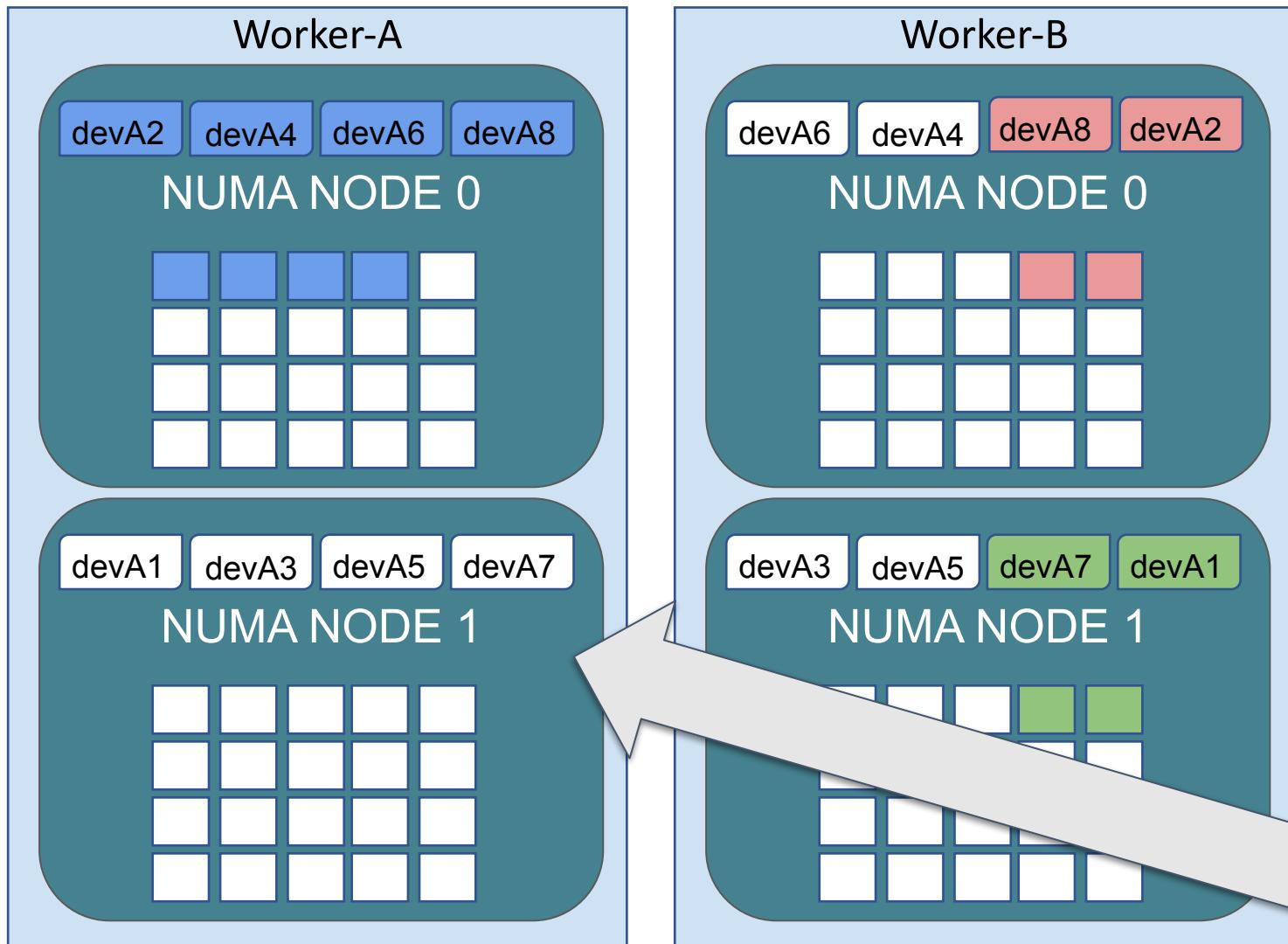
		Device A	CPU
Worker-A	Numa Node 0	0	16
	Numa Node 1	4	20
Worker-B	Numa Node 0	2	18
	Numa Node 1	2	18

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Current Status

1. PodResource API

- Proposed Pod Resource API enhancements ([KEP](#)) Merged 
- Introduce device topology and cpuids info as part of Podresource API ([PR](#)) Merged 
- GetAllocatableResources() support being targeted for K8s 1.21 release 

2. Node Feature Discovery

- Resource Topology Enablement (RTE) ([KEP](#), [code](#)) 
- Initial Discussions with NFD maintainers and stakeholders ([Issue](#), [Proposal Doc](#)) 
- NFD development work in progress [here](#) 
- Initial Demo: [here](#) 

3. Topology aware scheduler plugin ([KEP](#), [code](#))

4. NodeResourceTopology API ([PR](#)) - still being discussed in the community

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6. Use cases

For more detail: [Use case Doc.](#)

vRAN User plane (Nokia)

- requirement is to process packets with extreme high bandwidth. To achieve this we use DPDK with SR-IOV.
- The pods handling the user traffic require the SR-IOV VF, the memory huge pages and the CPU resources from the same NUMA node.

Performance-intensive high throughput network applications (Samsung)

- strict requirements must be met (of speed, latency, availability) for containerized 5G deployments
- MEC (Multi-access Edge Computing) - to deliver low latency access to edge applications

Cloudnative Network Function cluster level NUMA alignments (Intel)

Two workload numa alignments requirements:

- Full: CPU / HugePage / SRIOV VF have to be in the same NUMA Zone
- Native: CPU / HugePages must be aligned. SRIOV VFs can come from either of the NUMA zones.

GPUDirect scheduling (ViaSat)

- GPU, NIC, and CPU must be in the same NUMA zone.
- The GPU and NIC must be sharing a single-hop on the PCI bus

- ▶ Learn more: [here](#)
- ▶ Use Cases: [here](#)
- ▶ K8s slack:
<https://kubernetes.slack.com/archives/C012XSGFZQE>
- ▶ Email: swsehgal@redhat.com, Slack: swsehgal

Demos

- ▶ Topology aware scheduling
with Resource Topology
Exporter: [Demo](#)
- ▶ NFD exposing hardware
topology through CRDs:
[Demo](#)

GET INVOLVED



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